

WEST**End of Result Set**

Generate Collection

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L8: Entry 4 of 4

File: USPT

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DOCUMENT-IDENTIFIER: US 4642793 A

TITLE: Many-to-one mapping hash address generator

Brief Summary Paragraph Right (3):

The advantage of using a many-to-one mapping scheme in such a system is that it enables a large but sparsely populated address range to be effectively compressed into a much smaller range, thus reducing the size of the bit maps. However, as explained in those specifications, one problem with such a scheme is that the bit maps may occasionally produce spurious outputs, indicating that a data item has been tagged when in fact it has not. The number of spurious outputs may be reduced for any particular set of data items under consideration, by suitable choice of the transformation. Thus, if a first choice of transformation results in an unacceptably high number of spurious outputs, the hash coder may be modified to produce a different transformation which gives a lower number of spurious outputs.

Detailed Description Paragraph Right (13):

The indicator R is also applied to the address input of a random access memory 37 having four locations, each of which contains a prime number in the range 3-251. The contents of the addressed location of the memory 37 supply the hashing key K for the hash coding circuit 30.

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